

12/02/98  
jc545 U.S. PTO

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

KAZUTAKA SHIBATA and TSUNEMORI  
YAMAGUCHI

For: METHOD OF SURFACE-  
MOUNTING ELECTRONIC  
COMPONENTS

Attorney Docket  
No. 8046.037US0

San Francisco, California

Box Patent Application  
Assistant Commissioner for Patents  
Washington, D.C. 20231

By Express Mail No: EM552312033US  
Dated: December 2, 1998

PATENT APPLICATION TRANSMITTAL


Sir:

Transmitted herewith for filing is the patent application of inventors KAZUTAKA SHIBATA and TSUNEMORI YAMAGUCHI, for "METHOD OF SURFACE-MOUNTING ELECTRONIC COMPONENTS." Enclosed are:

1. Thirteen (13) pages of the specification, including nine (9) claims and an abstract.
2. Six (6) sheets of informal drawings.

The filing fee is deferred at this time, but will be calculated and sent later.

Dated: December 2, 1998.

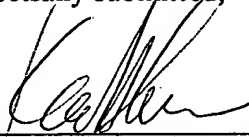
  
Keiichi Nishimura, Reg. No. 29,093  
MAJESTIC, PARSONS, SIEBERT & HSUE P.C.  
Four Embarcadero Center, Suite 1100  
San Francisco, California 94111-4106  
Telephone: (415) 248-5500  
Facsimile: (415) 362-5418

Atty. Docket: 8046.037US0

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The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 13-1030. This authorization is provided in duplicate.

Respectfully submitted,



Dated: February 24, 1999.

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Keiichi Nishimura (Reg. No. 29,093)  
MAJESTIC, PARSONS, SIEBERT & HSUE P.C.  
Four Embarcadero Center, Suite 1100  
San Francisco, CA 94111-4106  
Telephone: (415) 248-5500  
Facsimile: (415) 362-5418